Synopsys and AMD

A [illegible] 4, [illegible]B 3.0, C 3.1, [illegible] 14-[illegible]

AMD

[illegible] EP C [illegible] C [illegible] IP. C [illegible] D [illegible] IP [illegible] —Rolands Ezers, Sr. Director, I/O & Circuit Technology, AMD

Business

A [illegible] (A [illegible]) [illegible]

Challenges

[illegible] 14-[illegible]

Synopsys Solution

[illegible] 4/3

[illegible]B 3.0

C 3.0

[illegible]A A 6[illegible]

[illegible] (1000BA[illegible] -[illegible] /[illegible], 10[illegible]BA[illegible] -[illegible])

[illegible]

[illegible]

[illegible]A [illegible]

[illegible]A [illegible]

Benefits

B [illegible]C [illegible]

A [illegible]