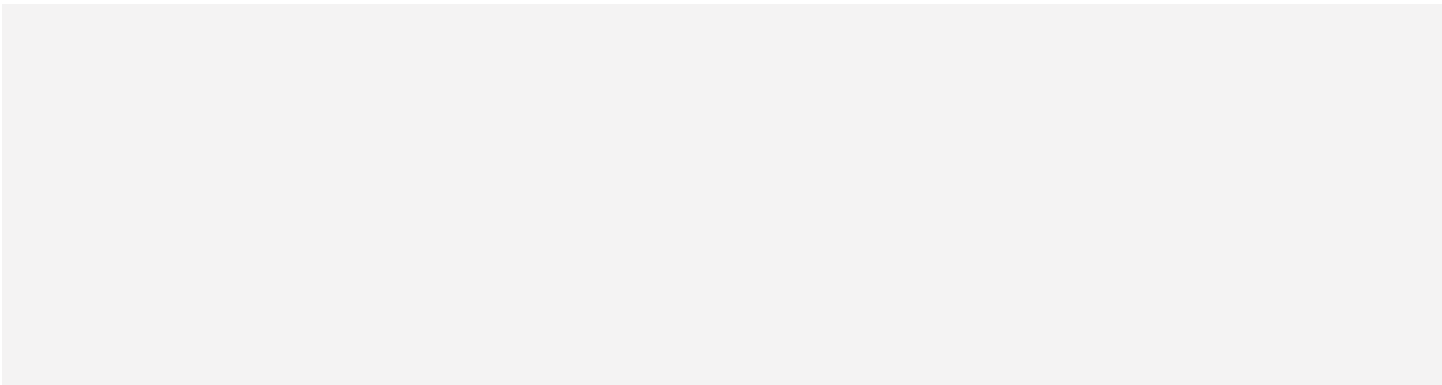


Every complex SoC design is created under significant time-to-market pressure. With the rise in software content as well as more IP (and more complex IP) being integrated, designers are faced with the challenge of meeting their performance, power and area targets without overdesigning their SoCs.

As part of your design team, Synopsys' SoC Architecture Design consultants will help you get your SoC off to the right start. They are ready to apply their expertise from years of designing mobile, automotive, netw



As both hardware and software complexity increases, you need more advanced and integrated IP solutions to meet your aggressive project schedules without compromising quality. Whether you need a single controller and PHY integration, a combination of multiple protocols, or complete subsystems with processors and the software stack, Synopsys experts deliver IP subsystems tuned to your SoC.

Synopsys' configurable, pre-verified IP Subsystems deliver complete, complex functions that are ready to integrate into your SoC as-is, or be customized by your team or ours.

By integrating specific IP blocks together in a single subsystem, Synopsys provides you with another option to reduce your design and integration effort, lower design risk, and accelerate time-to-market.

Your SoC's performance, floorplan, and pad ring requirements are unique, requiring customizable IP that meets your needs. While optimizing an implementation by hand can be challenging, as it involves analyzing and fine-tuning design parameters, Synopsys IP Hardening experts use an automated hardening flow to refine the implementation iteratively, for higher productivity and faster

