

Engineering teams are under constant pressure to get products to market faster without sacrificing quality. The insights locked in the data collected through design, manufacturing, test, and fabrication operations hide a wealth of information regarding the health and status of your SoC design. Tools in the Synopsys.ai data continuum family allow design teams to increase productivity across the full design-to-silicon lifecycle by connecting, analyzing, and drawing inference from this data.

Design debug and optimization times heavily dominate engineering schedules making it a challenge to meet the exacting product feature specifications and short time-to-market windows. There is a strong need to improve designer focus, productivity, and effectiveness. EDA metrics and tool-flow data are an under-valued and under-utilized gold mine of opportunity. Tapping into this data can enable designers to find and fix the right problems, faster.

Central to design data analysis is the Synopsys Design.da solution that leverages proprietary data collected during the design flow to bring unmatched productivity and a better, faster, and smarter way to achieve design convergence. Synopsys Design.da is the industry's first comprehensive data-visibility and machine intelligence-guided design optimization and signoff-closure solution. Combining open-source industry-standard databases with a cloud native architecture, Synopsys Design.da delivers better observability to help mitigate project risk, intelligently uncovers actionable data insights to enable more efficient designer workflows, and provides a knowledge framework for a continuously improving design process.



