

## Author

Jigesh Patel, Ph.D. | Senior Design Engineer | Intel | 2014-2018

## Classifying Behavior of the Process Parameter Variations

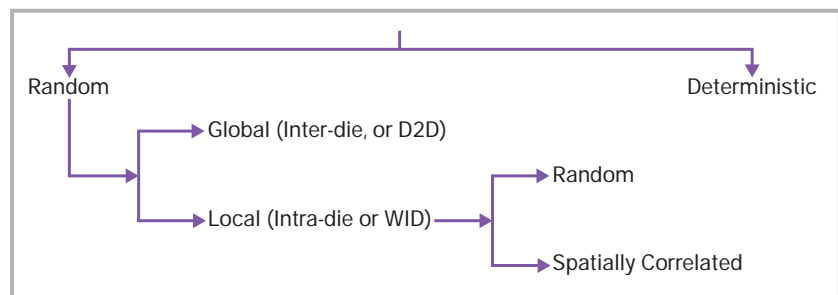


Figure 1: Independent and Correlated Process Parameter Variations in Silicon Photonics

Summarizing the above, process parameter variations in a parameter  $\theta$  can be modeled as:

$$\theta = \theta_0 + \delta_{D2D} + \delta_{WID, random} + \delta_{WID, correlated}$$

where  $\theta_0$  is the nominal value of the parameter  $\theta$ . Last three terms in the above expression represent die-to-die and

The mean and variance of  $\theta$  are:

Mean:

$$m_\theta = \theta_0$$

Variance:

$$\sigma_\theta^2 = \sigma_{D2D}^2 + \sigma_{WID, random}^2 + \sigma_{WID, correlated}^2$$

## Monte Carlo Process Variation and Corner Analyses in Synopsys OptoCompiler-OptSim

### Defining Parameters Using Statistical Expressions



Figure 2: Schematic of a 6-stage lattice filter (a). Each hierarchy comprises of three stages (b) where each stage is implemented as parametric custom photonic block (c)

Defining parameter G via a statistical expression, say, GAUSS(1 $\mu$ m,0.025 $\mu$ m) would imply that gap G follows a Gaussian distribution with 1 $\mu$ m mean and a 3  $\sigma$  deviation of 0.025 $\mu$ m relative to the mean. Process correlation can be specified via intermediate variable definitions. For example:

G1=GAUSS(0.1 $\mu$ m,0.025 $\mu$ m)

G\_wafer\_to\_wafer = AGAUSS(0.0,0.01 $\mu$ m)

If G\_wafer\_to\_wafer represents another random variable with zero mean and absolute 3  $\sigma$  deviation of 0.01 $\mu$ m, an expression like

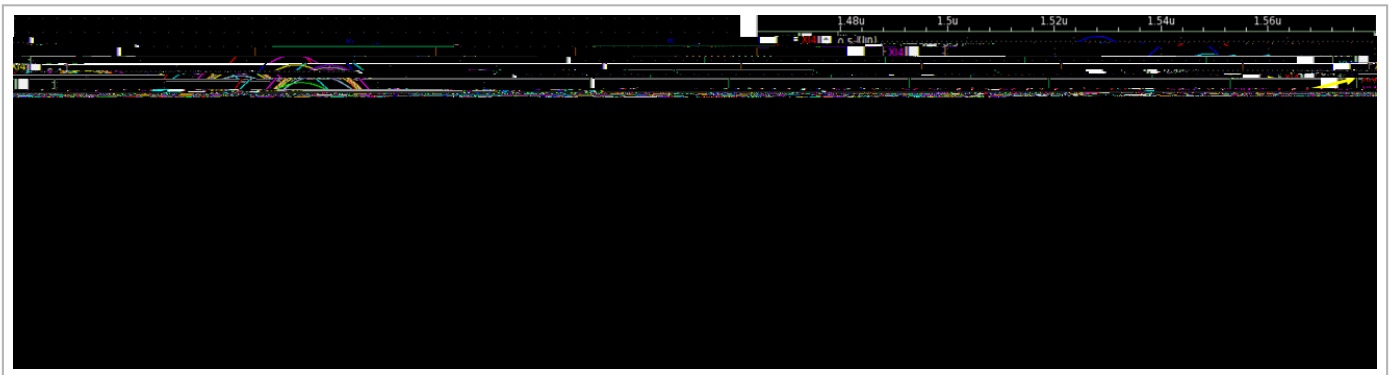


Figure 3: Filter response after 3- (upper right) and 6- (lower right) stages of lattice filters accounting for process parameter variations in gap and delay length

## Using Sub-Circuit Definitions

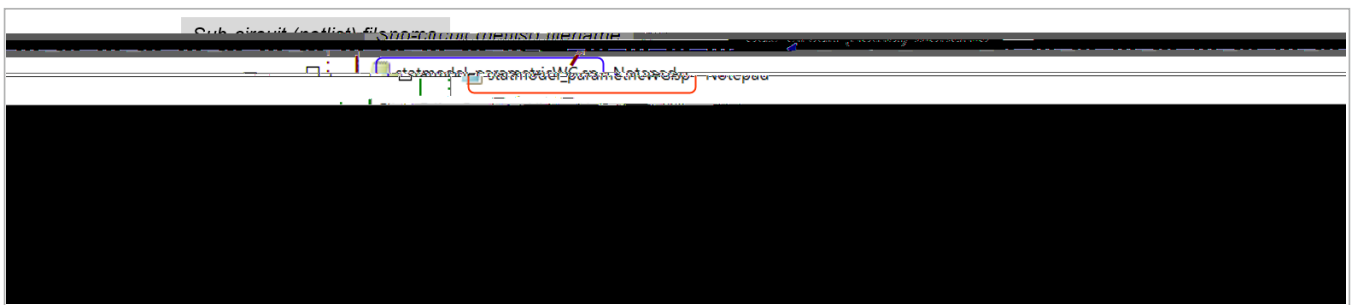


Figure 4: Example of a sub-circuit definition with process parameter variations

illustrated in Figure 5.

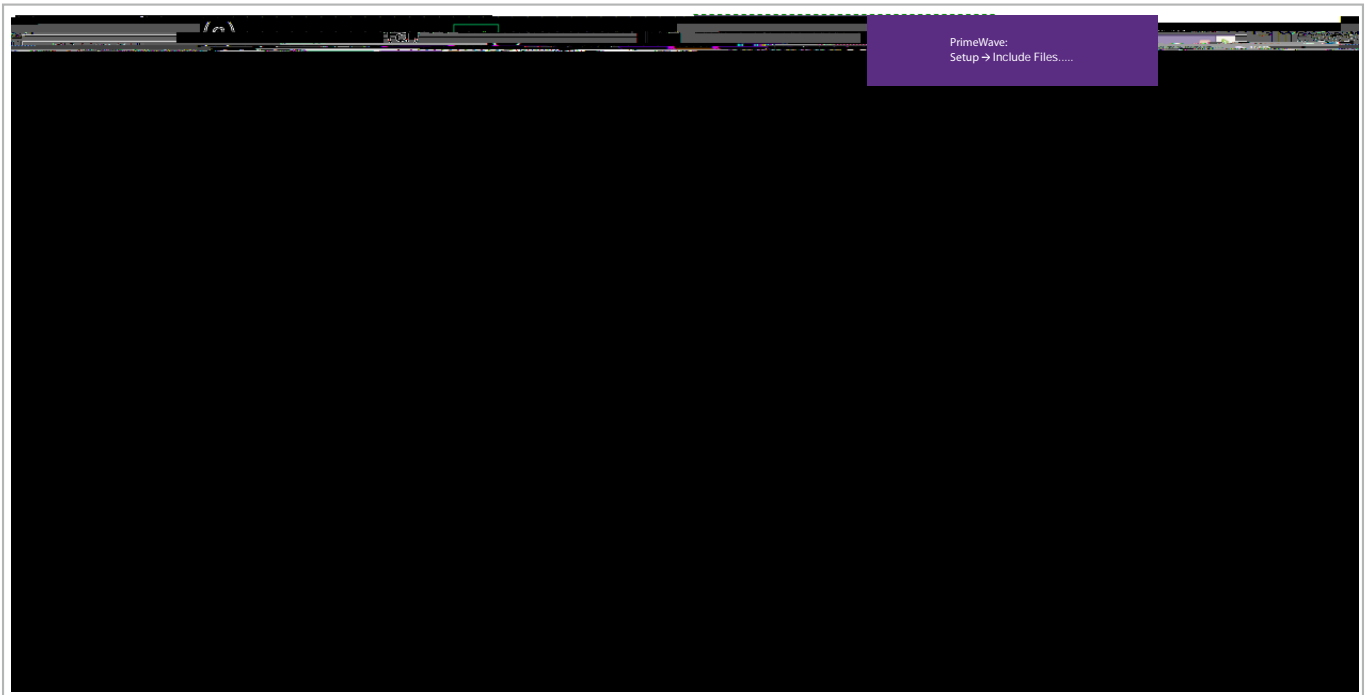


Figure 5: (a) A Mach-Zehnder Interferometer (MZI) comprising of two waveguide sub-circuits (b) including sub-circuit file of Figure 4 during run time and (c) MZI response due to process parameter variations

