John Koeter leads the IP Group at Synopsys. In this capacity, he leads the development, testing and support of Synopsys' Intellectual Property (IP) portfolio including interface, foundation, security and processor IP, and IP subsystems.

John brings more than 35 years of experience in semiconductor design, IP development, ASIC products, design services as a technologist and business leader. Before joining Synopsys, John held product management, engineering, design, marketing and application engineering positions with Texas Instruments and Advanced Micro Devices. He holds a BSEE degree from Cornell University.

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